

#3  
ATTORNEY'S DOCKET NO.

TI-33165

PAGE 1 OF 1



**APPLICATION FOR UNITED STATES PATENT  
DECLARATION AND POWER OF ATTORNEY**

COPY OF PAPERS  
ORIGINALLY FILED

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification of Application Serial No. 10/006,576, filed 12/06/01; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such-willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:  <b>Back Side Coating of Semiconductor Wafers</b>		
POWER OF ATTORNEY: <b>I HEREBY APPOINT PRACTITIONERS AT CUSTOMER NUMBER 23494 TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH</b>		
SEND CORRESPONDENCE TO:  Gary C. Honeycutt Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO:  Gary C. Honeycutt (972) 238-7160
NAME OF INVENTOR:  Dr. Sreenivasan K. Koduri	NAME OF INVENTOR:  Kenji Masumoto	NAME OF INVENTOR:  Mutsumi Masumoto
RESIDENCE & POST OFFICE ADDRESS:  801 Legacy Drive, Apt. 727 Plano, Texas 75023	RESIDENCE & POST OFFICE ADDRESS:  1969-26 Hiji Oita, Japan 879-1502	RESIDENCE & POST OFFICE ADDRESS:  17-9, Spaland Toyomi Beppu Oita, Japan 874-0012
COUNTRY OF CITIZENSHIP:  India	COUNTRY OF CITIZENSHIP:  Japan	COUNTRY OF CITIZENSHIP:  Japan
SIGNATURE OF INVENTOR:  <i>x K. Sreenivasan</i>	SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:
DATE:  <i>x 4 Jan 02</i>	DATE:	DATE:

PAGE 1 OF 1



ATTORNEY'S DOCKET NO.

TI-33165

COPY OF PAPERS  
ORIGINALLY FILED

**APPLICATION FOR UNITED STATES PATENT  
DECLARATION AND POWER OF ATTORNEY**

As a below named inventor, I declare that my residence, post office address and citizenship are as stated below next to my name; that I verily believe that I am the original, first and sole inventor if only one name is listed below, or an original, first and joint inventor if plural inventors are named below, of the subject matter which is claimed and for which a patent is sought on the invention entitled as set forth below, which is described in the attached specification of Application Serial No. 10/006,576, filed 12/06/01; that I have reviewed and understand the contents of the specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration; that no application for patent or inventor's certificate on this invention has been filed by me or my legal representatives or assigns in any country foreign to the United States of America; and that I acknowledge my duty to disclose information which is material to the patentability of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56;

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

TITLE OF INVENTION:		
Back Side Coating of Semiconductor Wafers		
POWER OF ATTORNEY: I HEREBY APPOINT PRACTITIONERS AT CUSTOMER NUMBER <b>23494</b> TO PROSECUTE THIS APPLICATION AND TRANSACT ALL BUSINESS IN THE PATENT AND TRADEMARK OFFICE CONNECTED THEREWITH		
SEND CORRESPONDENCE TO:  Gary C. Honeycutt Texas Instruments Incorporated P.O. Box 655474, MS 3999 Dallas, TX 75265		DIRECT TELEPHONE CALLS TO:  Gary C. Honeycutt (972) 238-7160
NAME OF INVENTOR:  Dr. Sreenivasan K. Koduri	NAME OF INVENTOR:  Kenji Masumoto	NAME OF INVENTOR:  Mutsumi Masumoto
RESIDENCE & POST OFFICE ADDRESS:  801 Legacy Drive, Apt. 727 Plano, Texas 75023	RESIDENCE & POST OFFICE ADDRESS:  1969-26 Hiji Oita, Japan 879-1502	RESIDENCE & POST OFFICE ADDRESS:  17-9, Spaland Toyomi Beppu Oita, Japan 874-0012
COUNTRY OF CITIZENSHIP:  India	COUNTRY OF CITIZENSHIP:  Japan	COUNTRY OF CITIZENSHIP:  Japan
SIGNATURE OF INVENTOR:	SIGNATURE OF INVENTOR:  <i>Kenji Masumoto</i>	SIGNATURE OF INVENTOR:  <i>Mutsumi Masumoto</i>
DATE:	DATE:  <i>Jan. 7 2002</i>	DATE:  <i>Jan. 7 2002</i>